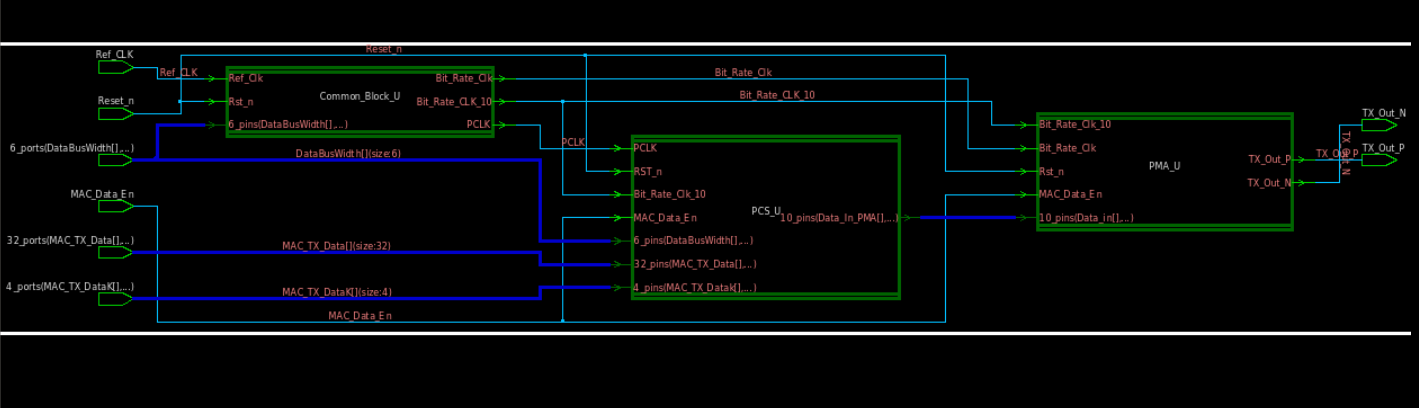
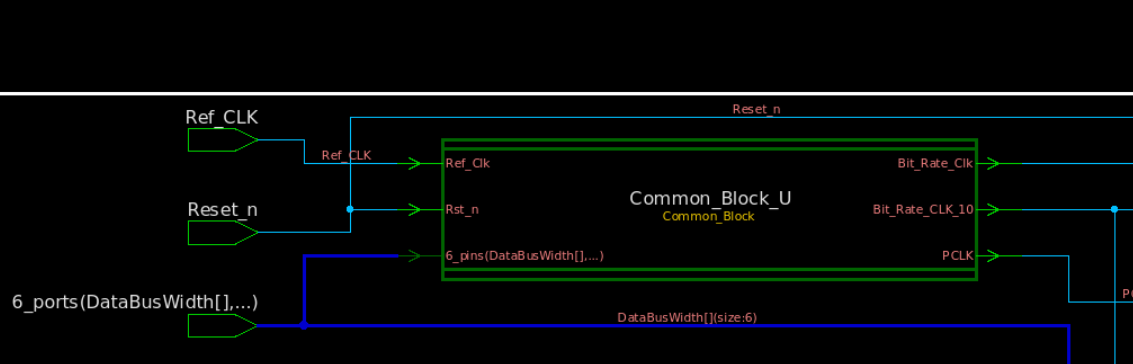
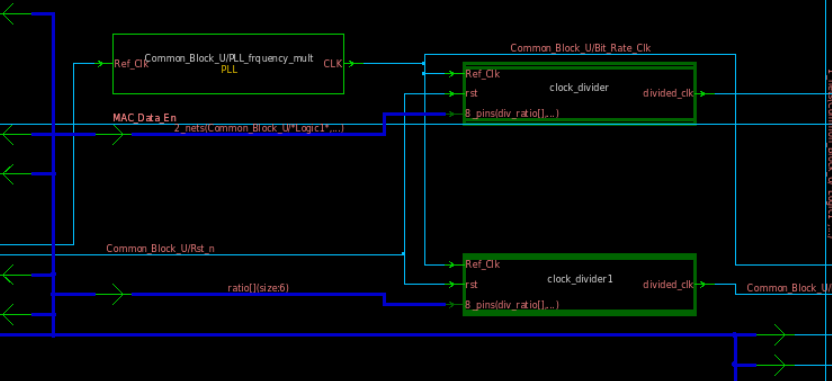
**TX Blocks**



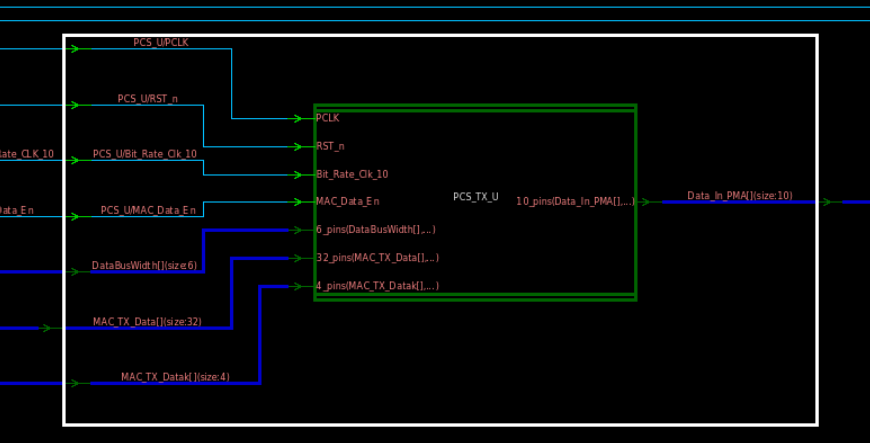




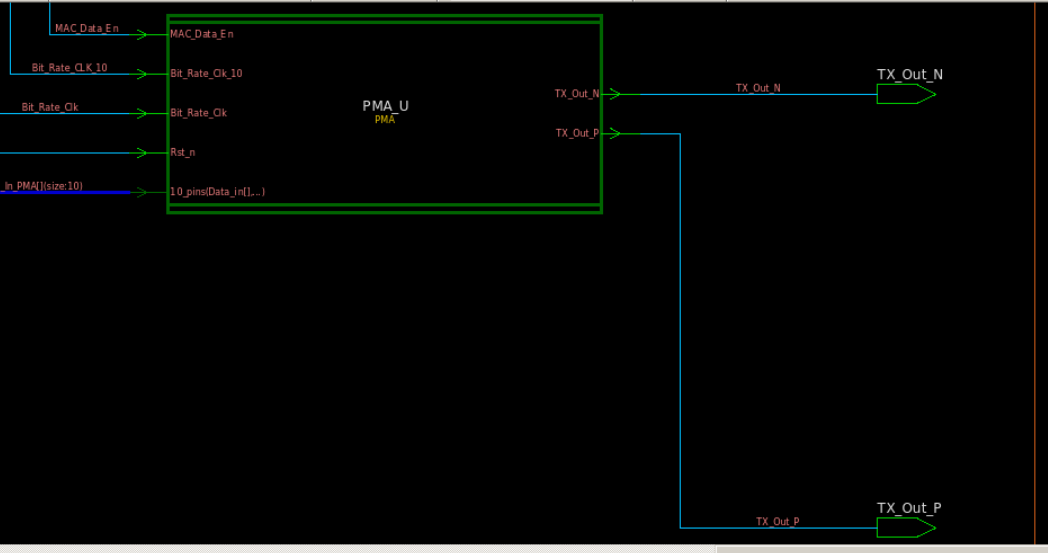


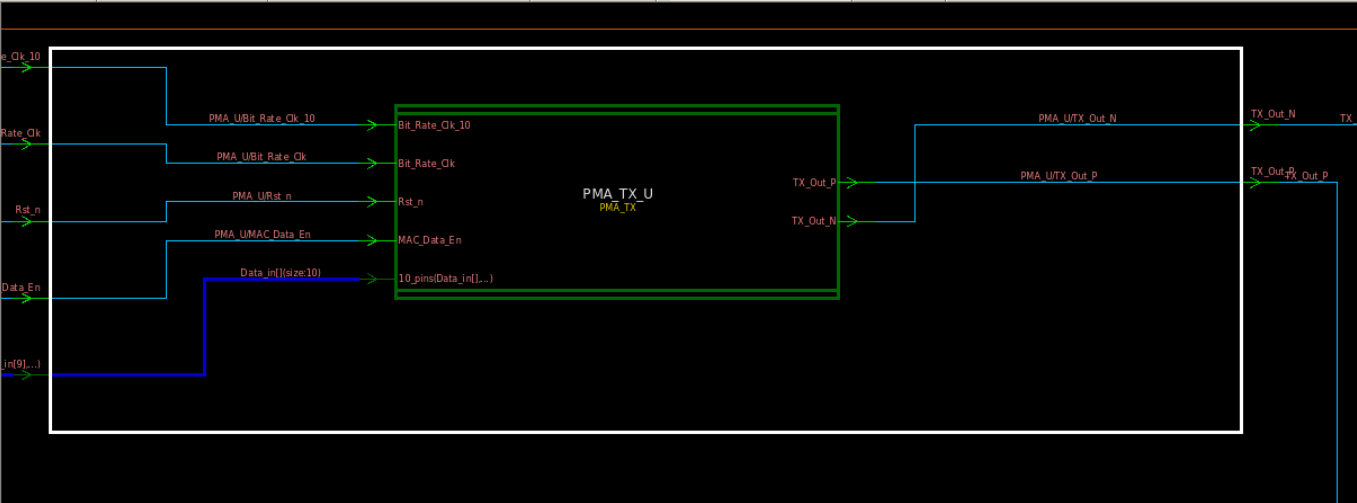
|  |  |  |
| --- | --- | --- |
| Port | Width | Type |
| Ref\_CLK | 1 | Input |
| Rst\_n | 1 | Input |
| DataBusWidth | 6 | Input |
| Bit\_Rate\_Clk | 1 | Output |
| Bit\_Rate\_CLK\_10 | 1 | Output |
| PCLK | 1 | Output |





|  |  |  |
| --- | --- | --- |
| Port | Width | Type |
| PCLK | 1 | Input |
| RST\_n | 1 | Input |
| Bit\_Rate\_Clk\_10 | 1 | Input |
| DataBusWidth | 6 | Input |
| MAC\_TX\_Data | 32 | Input |
| MAC\_TX\_Datak | 4 | Input |
| MAC\_Data\_En | 1 | Input |
| Data\_In\_PMA | 10 | Output |





|  |  |  |
| --- | --- | --- |
| Port | Width | Type |
| Bit\_Rate\_Clk\_10 | 1 | Input |
| Bit\_Rate\_Clk | 1 | Input |
| Rst\_n | 1 | Input |
| MAC\_Data\_En | 1 | Input |
| Data\_in | 10 | Input |
| Tx\_Out\_P | 1 | Output |
| Tx\_Out\_N | 1 | Output |